Please amend the second line of the table on page 6 as follows:

A2

	Analog circuit	Digital circuit
The number of transistors	Few (about 20 pcs in multiplier)	Many (2000 pcs in 8-bit multiplier)
Chip area	Small (few devices)	Large (many devices)
Power consumption	Low power consumption because of fewer devices	Large (many gates are switched)
Clock frequency	Low (determined by settling of amplifier)	Higher (1/2 of cut-off frequency of device)
Signal frequency	High (about 1/2 of cut-off frequency of device)	Low (1/10 of clock frequency)
Precision	Low (device deviation, noise)	High (depending on bit number)
Stability	Low (oscillation, characteristic variation)	High
Noise resistance	Low (S/N)	Strong (large noise margin)

Please amend the paragraph bridging pages 26-27 of the specification as follows:

A3

The gas flow detection circuit 10 outputs a voltage signal representing a gas flow passing through a gas passage. The gas flow detection circuit 10 may be a gas flow detection circuit DECT1 shown in Fig. 25 which detects a current flowing through a resistor arranged in the gas passage or a voltage across the resistor and outputs a voltage signal representing the gas flow passing through the gas passage.

Please amend the equation at line 10 of page 44 of the specification as follows:

A4

$$Dout = (a \cdot Dtemp + b) \cdot Din + (c \cdot Dtemp + d) \qquad (6)$$